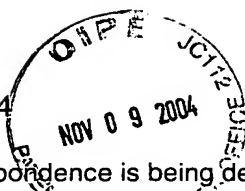


Docket No.: WMP-IFT-964



IFW

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By: 

Date: November 3, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/657,898 Confirmation No: 5647
Applicant : Thomas Steinecke et al.
Filed : September 9, 2003
Title : Electronic Device with a Voltage Supply Structure,
Semiconductor Wafer with Electronic Devices, and Associated
Production Methods
Art Unit : 2814
Examiner : Phat X. Cao

Docket No. : WMP-IFT-964
Customer No. : 24131

R E S P O N S E

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated October 12, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group I, claims 1-17, for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-17 is solicited.

Respectfully submitted,



For Applicants

WERNER H. STEMER

Date: November 3, 2004 REG. NO. 34,956

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